

8-5-04

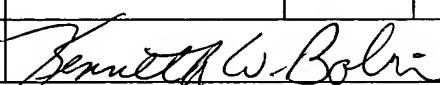
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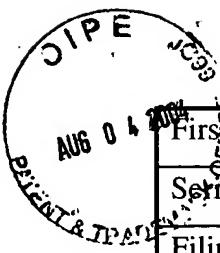


First Named Inventor	Leonard Forbes
Serial No.	10/682,590
Filing Date	October 9, 2003
Group Art Unit	2818
Examiner Name	Mai-Huong C. Tran
Confirmation Number	1986
Attorney Docket No.	400.257US01
Title: FULLY DEPLETED SILICON-ON-INSULATOR CMOS LOGIC	

**TRANSMITTAL
FORM UNDER
37 CFR 1.10
(LARGE ENTITY)**

Mail Stop: AMENDMENT
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Enclosures					
The following documents are enclosed:					
<input checked="" type="checkbox"/> Amendment and Response to Office Action (6 pgs.); <input checked="" type="checkbox"/> a return postcard.					
Please charge any additional fees or credit any overpayments to Deposit Account No. 501373.					
<u>CUSTOMER NUMBER 27073</u>					
Leffert Jay & Polglaze, P.A. P. O. Box 581009 Minneapolis, MN 55458-1009					
Submitted By					
Name	Kenneth W. Bolvin	Reg. No.	34,125	Telephone	(612) 312-2211
Signature				Date	August 4, 2004
Certificate of Mailing					
"Express Mail" mailing label number: <u>EV 306296253 US</u> Date of Deposit: <u>August 4, 2004</u> These papers and fees are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated above and addressed to Mail Stop: AMENDMENT, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.					
(LARGE ENTITY TRANSMITTAL UNDER 37 C.F.R. 1.10)					



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RESPONSE TO
NON-FINAL OFFICE
ACTION

Title: FULLY DEPLETED SILICON-ON-INSULATOR CMOS LOGIC

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In response to the non-final Office Action mailed July 16, 2004, please consider the following amendments and remarks: